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APPLICATION NO.	FILING DATE	FIRST NAMED INVENTOR	ATTORNEY DOCKET NO.	CONFIRMATION NO.
09/820,671	03/30/2001	Eiji Yanagawa	1248-0538P	4277

2292 7590 04/01/2002

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EXAMINER

CLARK, SHEILA V

ART UNIT	PAPER NUMBER
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2815

DATE MAILED: 04/01/2002

Please find below and/or attached an Office communication concerning this application or proceeding.

# Office Action Summary

Application No.  
09/820,671

Applicant(s)  
Yangawa et al

Examiner  
Sheila V. Clark

Art Unit  
2815



-- The MAILING DATE of this communication appears on the cover sheet with the correspondence address --

## Period for Reply

A SHORTENED STATUTORY PERIOD FOR REPLY IS SET TO EXPIRE 3 MONTH(S) FROM THE MAILING DATE OF THIS COMMUNICATION.

- Extensions of time may be available under the provisions of 37 CFR 1.136 (a). In no event, however, may a reply be timely filed after SIX (6) MONTHS from the mailing date of this communication.
- If the period for reply specified above is less than thirty (30) days, a reply within the statutory minimum of thirty (30) days will be considered timely.
- If NO period for reply is specified above, the maximum statutory period will apply and will expire SIX (6) MONTHS from the mailing date of this communication.
- Failure to reply within the set or extended period for reply will, by statute, cause the application to become ABANDONED (35 U.S.C. § 133).
- Any reply received by the Office later than three months after the mailing date of this communication, even if timely filed, may reduce any earned patent term adjustment. See 37 CFR 1.704(b).

## Status

- 1) ☐ Responsive to communication(s) filed on \_\_\_\_\_.
- 2a) ☐ This action is **FINAL**. 2b) ☒ This action is non-final.
- 3) ☐ Since this application is in condition for allowance except for formal matters, prosecution as to the merits is closed in accordance with the practice under *Ex parte Quayle*, 1935 C.D. 11; 453 O.G. 213.

## Disposition of Claims

- 4) ☒ Claim(s) 1-20 is/are pending in the application.
- 4a) Of the above, claim(s) \_\_\_\_\_ is/are withdrawn from consideration.
- 5) ☒ Claim(s) 10-20 is/are allowed.
- 6) ☒ Claim(s) 1-9 is/are rejected.
- 7) ☐ Claim(s) \_\_\_\_\_ is/are objected to.
- 8) ☐ Claims \_\_\_\_\_ are subject to restriction and/or election requirement.

## Application Papers

- 9) ☐ The specification is objected to by the Examiner.
- 10) ☐ The drawing(s) filed on \_\_\_\_\_ is/are objected to by the Examiner.
- 11) ☐ The proposed drawing correction filed on \_\_\_\_\_ is: a) ☐ approved b) ☐ disapproved.
- 12) ☐ The oath or declaration is objected to by the Examiner.

## Priority under 35 U.S.C. § 119

- 13) ☒ Acknowledgement is made of a claim for foreign priority under 35 U.S.C. § 119(a)-(d).
- a) ☒ All b) ☐ Some\* c) ☐ None of:
1. ☒ Certified copies of the priority documents have been received.
  2. ☐ Certified copies of the priority documents have been received in Application No. \_\_\_\_\_.
  3. ☐ Copies of the certified copies of the priority documents have been received in this National Stage application from the International Bureau (PCT Rule 17.2(a)).
- \*See the attached detailed Office action for a list of the certified copies not received.
- 14) ☐ Acknowledgement is made of a claim for domestic priority under 35 U.S.C. § 119(e).

## Attachment(s)

- 15) ☒ Notice of References Cited (PTO-892) 18) ☐ Interview Summary (PTO-413) Paper No(s). \_\_\_\_\_
- 16) ☐ Notice of Draftsperson's Patent Drawing Review (PTO-948) 19) ☐ Notice of Informal Patent Application (PTO-152)
- 17) ☒ Information Disclosure Statement(s) (PTO-1449) Paper No(s). 5 20) ☐ Other:

Serial No: 09/820,671

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Claims 3-9 are rejected under 35 U.S.C. 112, second paragraph, as being indefinite for failing to particularly point out and distinctly claim the subject matter which applicant regards as the invention.

In claims 3 and 7, it is unclear what is meant by “receives such stress”. What type of stress does “such” refer? Also what type of “processing” is being referred? A chip may be involved which several types of processing.

In claims 4 and 9, “where the semiconductor element is processed” has not antecedent. Claim 3 has recited not particular area of the element that is particularly processed.

In claim 6, it is unclear what is meant by level “part”. Claim 1 recites that the entire chip level not certain part.

In claim 7, the entire claim for the most part is riddled with poor sentence structure and unclear grammatical issues rendering the claim unclear. The phrase “after securing” is unclear. This seems to refer to some previous securing step. There is no previous step. The “step or processing” is unclear. What processing step is being referred and the “processing step” has no antecedent. In the last line to what component does “a part” refer?

In claims 8 and 9, “the processing step” has no antecedent.

The following is a quotation of the appropriate paragraphs of 35 U.S.C. 102 that form the basis for the rejections under this section made in this Office action:

A person shall be entitled to a patent unless --

(e) the invention was described in a patent granted on an application for patent by another filed in the United States before the invention thereof by the applicant for patent, or on an international application by another who has fulfilled the requirements of paragraphs (1), (2), and (4) of section 371© of this title before the invention thereof by the applicant for patent.

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Claims 1-2 are rejected under 35 U.S.C. 102(e) as being anticipated by Applicant's Prior Art Figure 19 and the Admitted Prior Art in the disclosure of page 5.

A semiconductor element 102 is disclosed on page 5, last paragraph as being secured level onto board 103. Said device is taught to operate normally when packaged on the board.

Said device is shown to be of a flip chip type.

Claims 1-9 are rejected.

Claims 5, 6, 8 would be allowable if rewritten or amended to overcome the rejection(s) under 35 U.S.C. 112, 2<sup>nd</sup> paragraph, set forth in this Office action including all of the limitations of the base claim and any intervening claims.

Claims 10-20 are considered allowable over the prior art of record.

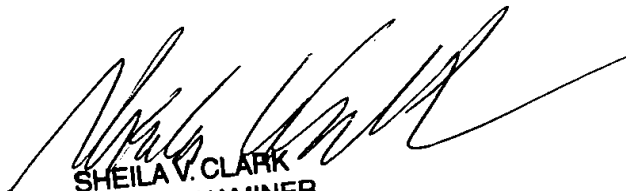
Kurosawa et al, Toyoda et al, Piosenka et al and Barton et al are cited to show semiconductor devices having detector element.

Any inquiry concerning this communication or earlier communications from the examiner should be directed to Examiner S.V. Clark whose telephone number is (703) 308-4924.

If attempts to reach the examiner by telephone are unsuccessful, the examiner's supervisor, Eddie Lee, can be reached on (703) 308-1690. The fax phone number for the organization where this application or proceeding is assigned is (703) 308-7722 or 7724.

Any inquiry of a general nature or relating to the status of this application or proceeding should be directed to the receptionist whose telephone number is (703) 308-0956.

March 20, 2002

  
SHEILA V. CLARK  
PRIMARY EXAMINER